

	Type	L #	Hits	Search Text	DBs
2	BRS	L2	0	(second adj (phase adj lock\$2 adj loop PLL) near2 (coupl\$3 connect\$3) near2 first adj (phase adj lock\$2 adj loop PLL) with bias adj circuit with second adj power adj supply).clm.	US-PGPUB
4	BRS	L4	1	((phase adj lock\$2 adj loop PLL) with analog adj power adj supply with digital adj power adj supply and (VCO voltage adj controlled adj oscillator) with digital adj power adj supply).clm.	US-PGPUB
5	BRS	L5	0	(second adj (phase adj lock\$2 adj loop PLL) near2 (coupl\$3 connect\$3) near2 first adj (phase adj lock\$2 adj loop PLL) with second adj power adj supply and multiplexer with output with first adj (phase adj lock\$2 adj loop PLL)).clm.	US-PGPUB